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Claims

1. Method of operating multiple (n) parallel-connected pulse-controlled inverters (1, 2),

characterized in that

the individual current(s) of the (n) pulse-controlled inverters (1, 2), or of a number reduced by 1 (n-1) of pulse-controlled inverters (1, 2) is/are regulated.
2. Method according to Claim 1, characterized in that the individual currents from two pulse-controlled inverters (1, 2) are regulated.
3. Method according to Claims 1 or 2, characterized by pulse-controlled inverters (1, 2) of the same output.
4. Method according to Claim 3, characterized in that the total current is uniformly distributed among pulse-controlled inverters (1, 2) of the same output.
5. Method according to one of the foregoing claims, characterized in that each pulse-controlled inverter (1, 2) is regulated separately.
6. Method according to one of the foregoing claims, characterized in that the input variable of regulation is generated by the difference between the setpoint value

and the actual value of the corresponding output current, and by the modulation pattern.

7. Method according to one of the foregoing claims, characterized in that the control edges of the power semiconductors (T11, T14, T21, T24) are shifted within the pulse-controlled inverter(s) (1, 2).
8. Method according to one of the foregoing claims, characterized in that each phase of one, of multiple, or of all pulse-controlled inverters (1, 2) is regulated individually.
9. Method according to one of the foregoing claims, characterized in that the gain factors (K1, K2) of regulation are dependent on external limiting conditions.